

Docket No.: 1999P1177 P



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By:

Date: September 18, 2001

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Holger Sedlak et al.
Applic. No. : 09/928,011
Filed : August 10, 2001
Title : Microprocessor and Method of Addressing in a Microprocessor

INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner of Patents and Trademarks,
Washington, D.C. 20231

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

U. S. Patent 4,821,183 (Hauris) dated April 11, 1989;

U. S. Patent 5,317,745 (Chan) dated May 31, 1994;

European Patent Application EP 0 709 767 A1 (Borrill) dated May 1, 1996;

European Patent Application EP 0 747 808 A2 (Goetz et al.) dated December 11, 1996;

European Patent Application EP 0 286 260 A2 (Lenoski) dated October 12, 1988;

German Published, Non-Prosecuted Patent Application DE 29 39 175 A1
(Macmillan) dated April 23, 1981, pertains to an arrangement for the determination of
addressing mode of work register set in microprocessors;

"Method for Calculating a Jump Address in a Microprocessor" 700 IBM Technical
Disclosure Bulletin 31 (1989) Jan. No. 8, Armonk, NY, US. Pages 371 - 372.

If no translation of pertinent portions of any foreign language patents or publications
mentioned above is included with the aforementioned copies of those applications,
patents and/or publications, it is because no existing translation is readily available to
the Applicants.

Respectfully submitted,



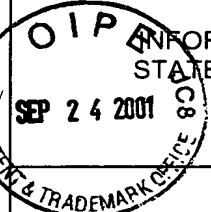
For Applicants

WERNER H. STEMER
REG. NO. 34,956

Date: September 18, 2001

Lerner And Greenberg, P.A.
Post Office Box 2480
Hollywood, FL 33022-2480
Tel: (954) 925-1100
Fax: (954) 925-1101

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| FORM PTO-1449 (SUBSTITUTE) U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE | | | | Attorney Docket No.: Applic. No. 1999P1177 P 09/928,011 | | | |
|  INFORMATION DISCLOSURE STATEMENT BY APPLICANTS (37 CFR 1.98(b)) | | | | Applicants Holger Sedlak, Oliver Kniffler | | | |
| Filing Date August 10, 2001 | | | | Group Art Unit 0.0 | | | |
| U.S. PATENT DOCUMENTS | | | | | | | |
| EXAMINER INITIALS | | PATENT NO. | DATE | PATENTEE | CLASS | SUB CLASS | FILING DATE |
| | A | 4,821,183 | 04/89 | Hauris | | | |
| | B | 5,317,745 | 05/94 | Chan | | | |
| | C | | | | | | |
| | D | | | | | | |
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| FOREIGN PATENT DOCUMENT | | | | | | | |
| | | DOCUMENT NO. | DATE | COUNTRY | CLASS | SUB CLASS | TRANSL. YES NO |
| | J | 0 709 767 A1 | 05/96 | Europe | | | X |
| | K | 0 747 808 A2 | 12/96 | Europe | | | X |
| | L | 0 286 260 A2 | 10/88 | Europe | | | X |
| | M | 29 39 175 A1 | 04/81 | Germany | | | X |
| | N | | | | | | |
| OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.) | | | | | | | |
| | O | "Method for Calculating a Jump Address in a Microprocessor" 700 IBM Technical Disclosure Bulletin 31 (1989) Jan. No. 8, Armonk, NY, US. Pages 371 – 372. | | | | | |
| | P | | | | | | |
| EXAMINER | | | | DATE CONSIDERED | | | |
| EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicants. | | | | | | | |